

Model Name: P35-DS3L Rev1.0

SHEET

TITLE

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24	PCI EXPRESS*1 ,PCI SLOT 1,2
25	ITE8718/GB,RESET DRIVE
26	COM,LPT,PROCHOT
27	BIOS,CI,HWM,KB/MS

SHEET

TITLE

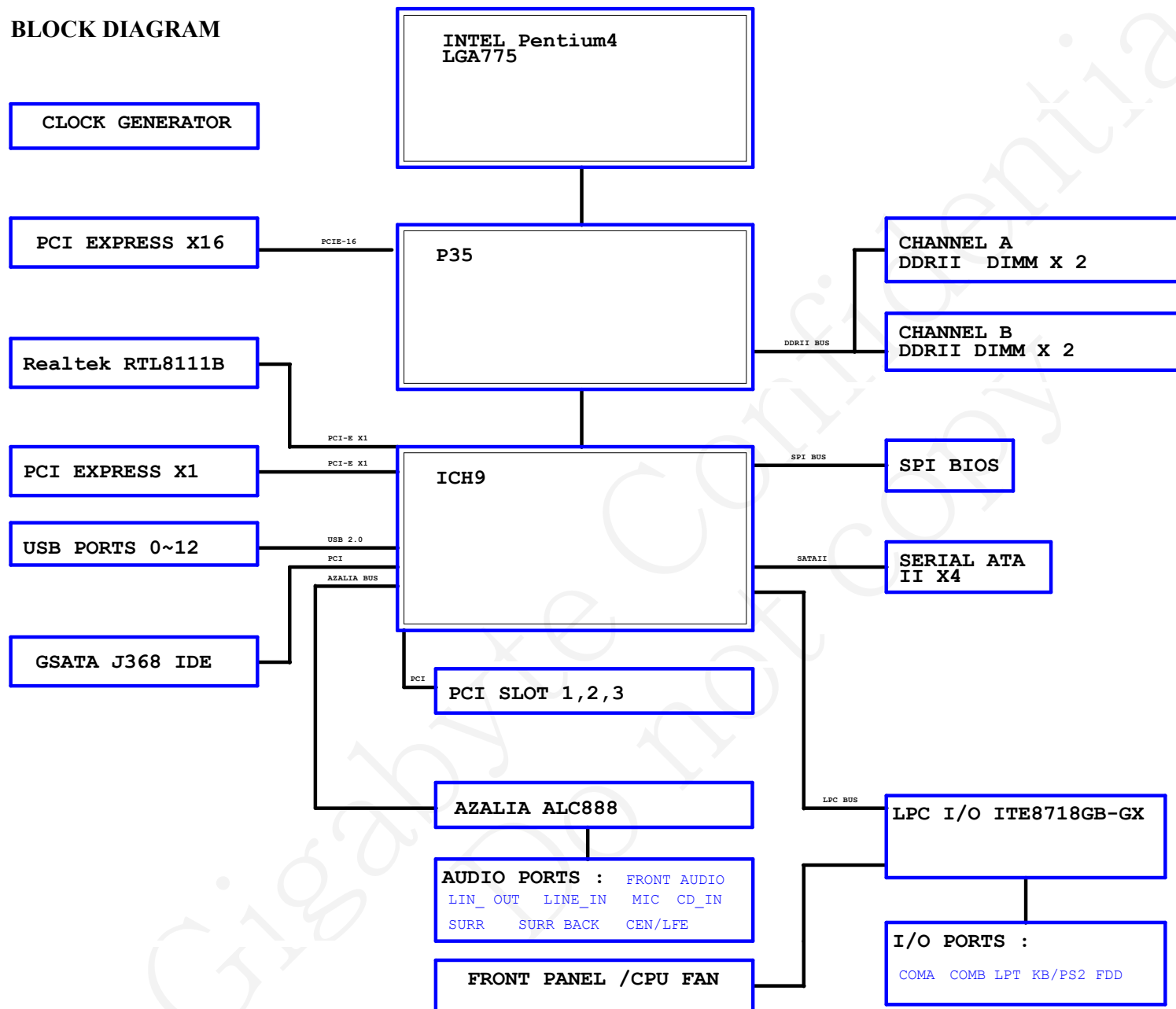
28	AZALIA ALC888
29	AUDIO JACK
30	VCORE PWM ISL6312
31	DISCRETE POWER
32	ATX POWER
33	JMicron JMB368
34	LAN REALTEK RTL8111B
35	FRONT PANEL,FUSB,FDD

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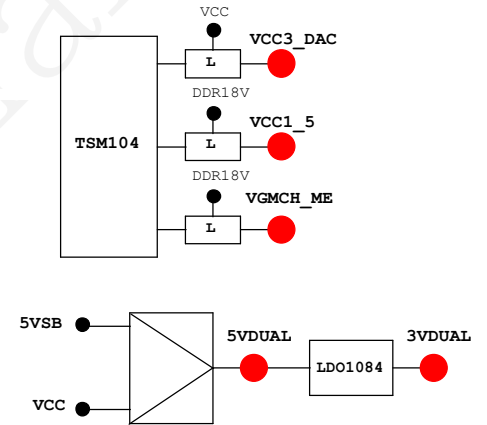
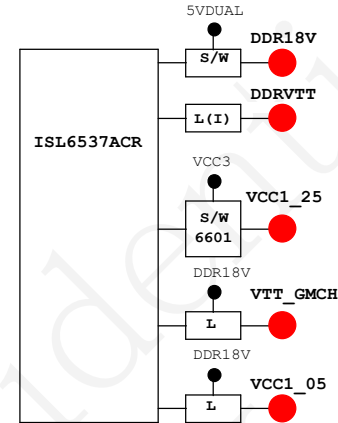
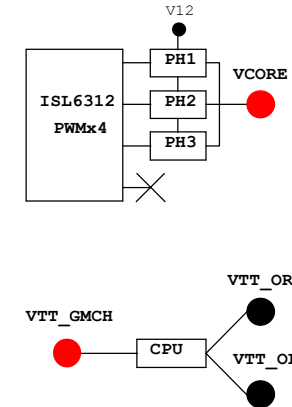
BLOCK DIAGRAM



ICH8 GPIO LIST TABLE

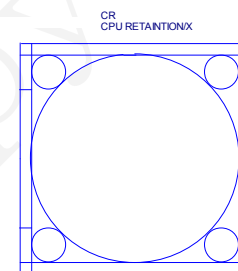
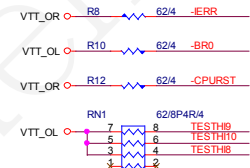
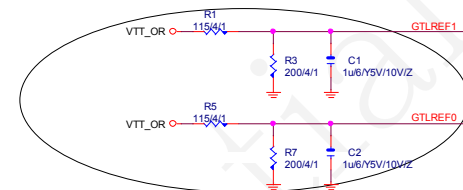
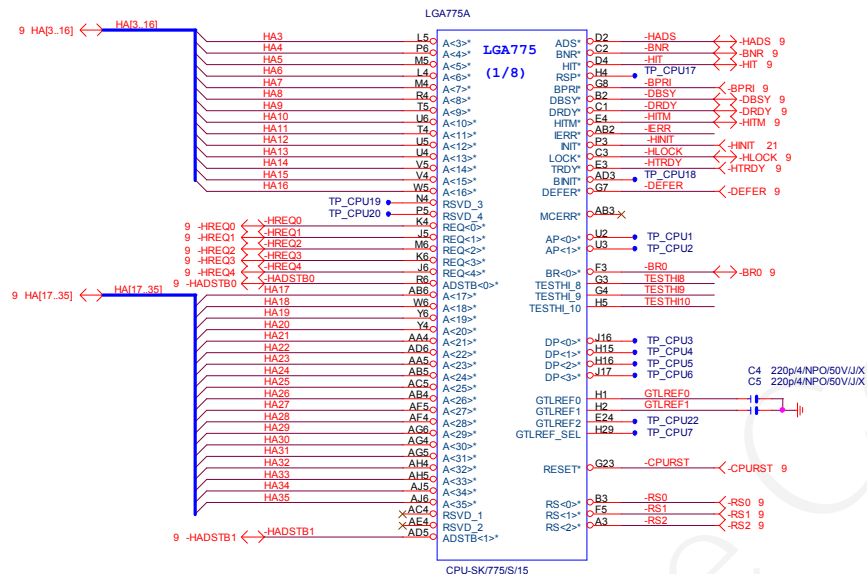
PIN NAME	PWR WELL	AFTER/ PLTRST	USAGE	NOTE
GP0	MAIN	IN	-ACZ_DET	P/U 8.2K VCC3
GP1/TACH1	MAIN	IN	ICH_FAN_TACH1	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	ICH_FAN_TACH2	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	ICH_FAN_TACH3	P/U 8.2K VCC3
GP8	STBY	IN	GPIO8 (DUALBIOS_INPUT)	P/U 8.2K 3VDUAL
GP9	STBY	OUT	WOL_ONLY	P/D 100K GND
GP10	STBY	IN	CLGPIO1	P/U 8.2K 3VDUAL
GP11/SMBALERT#	STBY	OUT	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	IN	MB_ID0	P/U 8.2K 3VDUAL
GP13	STBY	IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	IN	CLGPIO2	P/U 8.2K 3VDUAL
GP15	STBY	OUT	LAN_DISABLE (STP_PCI-)	N/A
GP16	MAIN	OUT/LOW	RESET	N/A
GP17/TACH0	MAIN	IN	ICH_FAN_TACH0	P/U 8.2K VCC3
GP18	MAIN	OUT	MB_ID1	P/U 8.2K VCC3
GP19	MAIN	IN	SATA1GP	P/U 8.2K VCC3
GP20	MAIN	OUT	-SPI_WF0	P/U 1K 3VCL
GP21	MAIN	IN	SATA0GP	P/U 8.2K VCC3
GP22	MAIN	IN	SCLOCK	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	CLGPIO0	P/U 8.2K 3VDUAL
GP25	STBY	IN	MB_ID2 (STP_CPU-)	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	S4_STATE#	P/U 8.2K 3VDUAL
GP27	STBY	OUT/LOW	GPIO27 (EL_STATE0)	P/U 8.2K 3VDUAL
GP28	STBY	OUT/LOW	PWR_LED (EL_STATE1)	N/A
GP29/OC5#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP30/OC6#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP31/OC7#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP32	MAIN	OUT	DUAL_BIOS	P/U 100K+1M VCC3
GP33	MAIN	OUT	DUAL_BIOS	P/U 8.2K VCC3
GP34	MAIN	OUT/LOW	GPIO34/SMB_RST	N/A
GP35	MAIN	OUT	SATACLKREQ#	N/A
GP36	MAIN	IN	SATA2GP	P/U 8.2K VCC3
GP37	MAIN	IN	SATA3GP	P/U 8.2K VCC3
GP38	MAIN	IN	SLOAD	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	GPIO48	P/U 8.2K VCC3
GP49	MAIN	IN	CPUPWROK	P/U 100 VTT_OL

VCORE:3 PHASE PWM--ISL6312

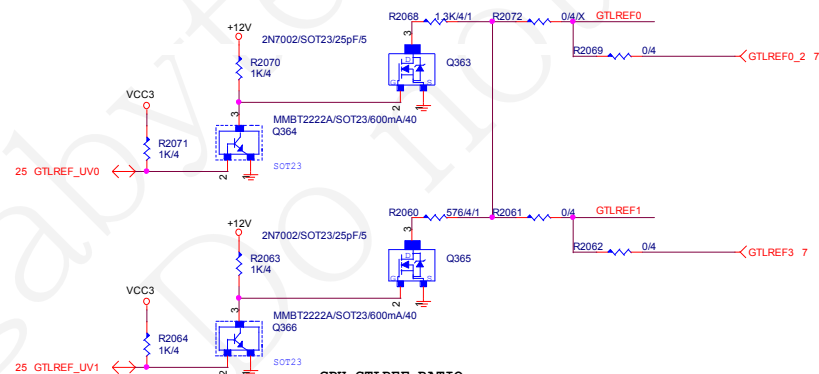
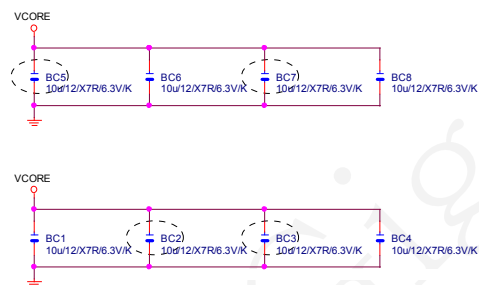


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Title			
TABLE LIST			
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HA/REQ:4/11 50+-15%
ADSTB:4/14 50+-15%

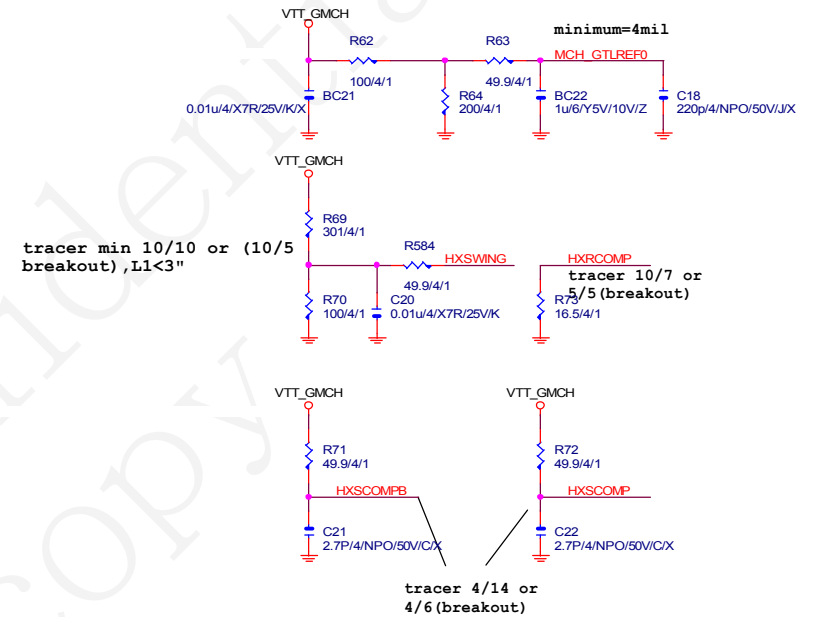
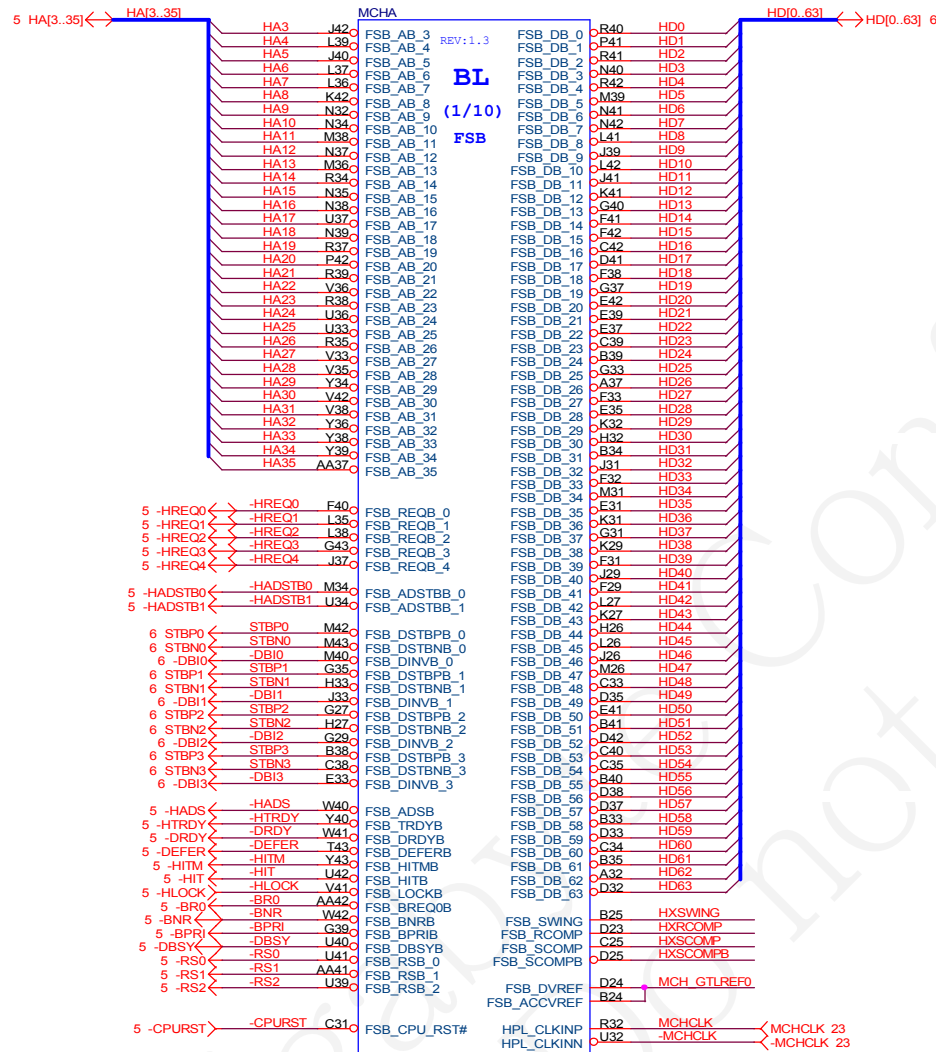


Impedance=50 +/- 15% for 4 -layer



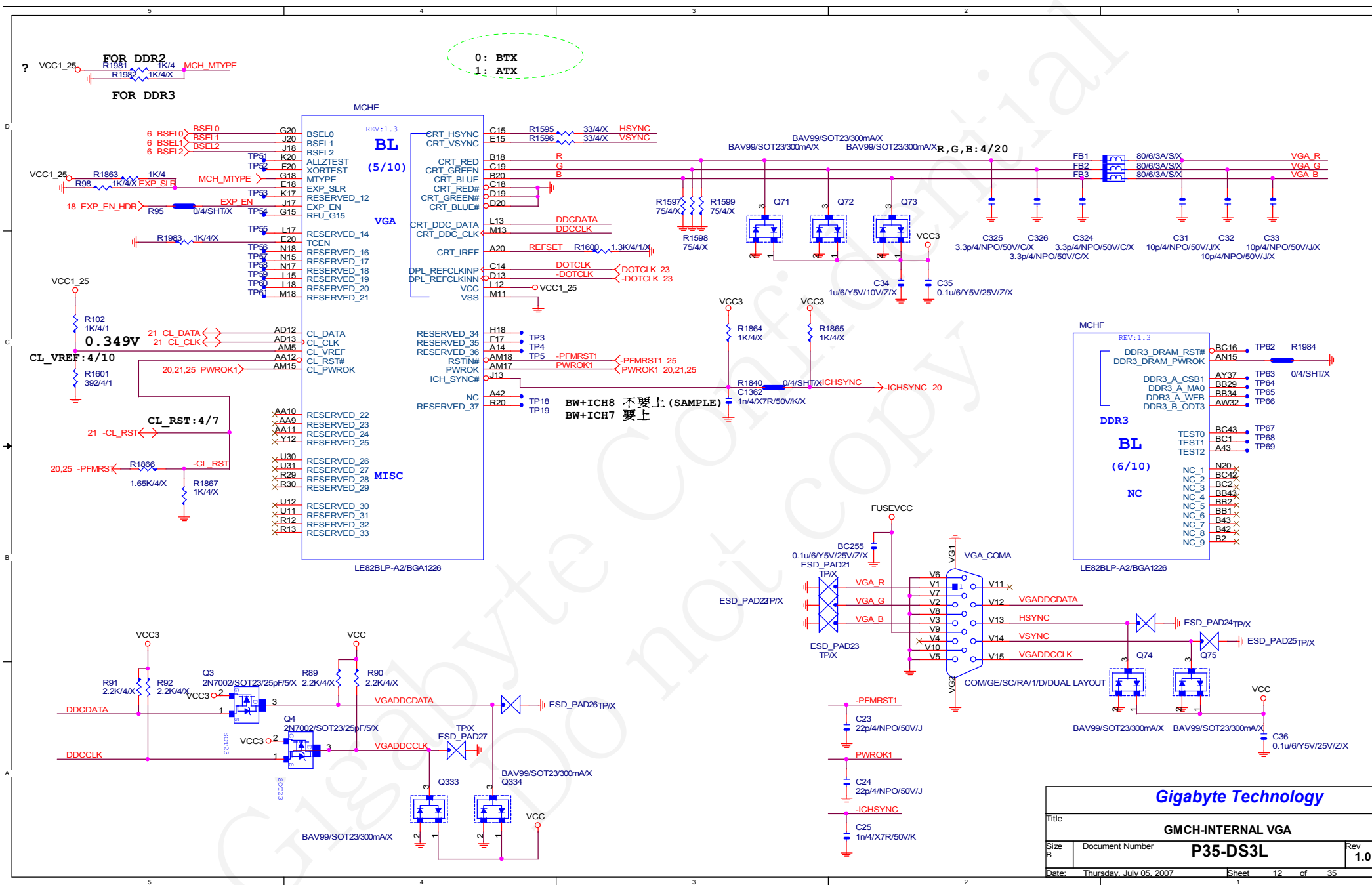
CPU GTLREF RATIO		
GTLREF UV0	GTLREF UV1	Ratio Set
HIGH	HIGH	0.67
LOW	HIGH	0.65
HIGH	LOW	0.63
LOW	LOW	0.615

			
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P4_LGA775-A			
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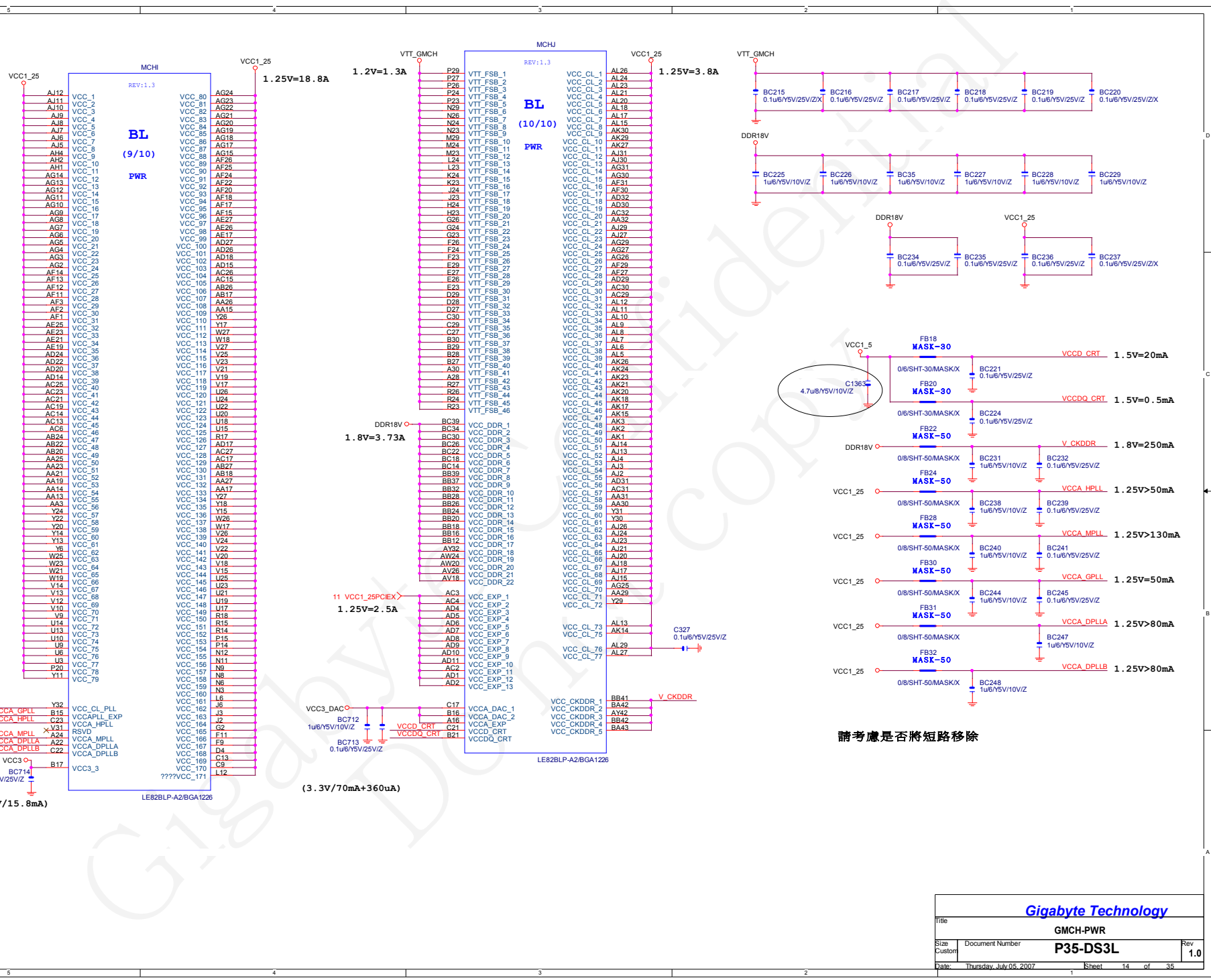
MCHG		
REV:1.3		
BC37	VSS_1	AE24
BC32	VSS_2	AE22
BC28	VSS_3	AE20
BC24	VSS_4	AE4
BC10	VSS_5	AE2
BC5	VSS_6	AD42
BB7	VSS_7	AD39
AY41	VSS_8	AD37
AY4	VSS_9	AD35
AW43	VSS_10	AD33
AW41	VSS_11	AD31
AV37	VSS_12	AD25
AV35	VSS_13	AD23
AV27	VSS_14	AD21
AV23	VSS_15	AD19
AV21	VSS_16	AC38
AV17	VSS_17	AC35
AV11	VSS_18	AC24
AV9	VSS_19	AC22
AV7	VSS_20	AC20
AV2	VSS_21	AC10
AU42	VSS_22	AC7
AU38	VSS_23	AC5
AU32	VSS_24	AB43
AU24	VSS_25	AB25
AU20	VSS_26	AB23
AU6	VSS_27	AB21
AU4	VSS_28	AB19
AT31	VSS_29	AB2
AT29	VSS_30	AB1
AT15	VSS_31	AA38
AT13	VSS_32	AA35
AT12	VSS_33	AA24
AR38	VSS_34	AA22
AR33	VSS_35	AA20
AR32	VSS_36	AA9
AR27	VSS_37	AA5
AR26	VSS_38	Y42
AR23	VSS_39	Y37
AR21	VSS_40	Y35
AR20	VSS_41	Y33
AR17	VSS_42	Y25
AR9	VSS_43	Y23
AR6	VSS_44	Y21
AP43	VSS_45	Y19
AP24	VSS_46	Y10
AP18	VSS_47	Y7
AP1	VSS_48	Y5
AN38	VSS_49	G32
AN31	VSS_50	G21
AN29	VSS_51	G13
AN24	VSS_52	G12
AN23	VSS_53	G11
AN20	VSS_54	G9
AN13	VSS_55	G7
AN12	VSS_56	G1
AN11	VSS_57	F37
AN4	VSS_58	F35
AM42	VSS_59	F27
AM40	VSS_60	F21
AM36	VSS_61	F18
AM33	VSS_62	F15
AM29	VSS_63	F3
AM24	VSS_64	E43
AM23	VSS_65	E32
AM20	VSS_66	E24
AM11	VSS_67	E21
AM9	VSS_68	E11
AM7	VSS_69	E9
AM4	VSS_70	E3
AL36	VSS_71	D40
AL33	VSS_72	D31
AL31	VSS_73	D21
AK43	VSS_74	D17
AJ39	VSS_75	D15
AJ36	VSS_76	D3
AJ33	VSS_77	C26
AJ32	VSS_78	C11
AH42	VSS_79	P43
AG37	VSS_80	P30
AG34	VSS_81	C6
AF43	VSS_82	C5
AF37	VSS_83	P18
AF36	VSS_84	P17
AF10	VSS_85	P2
AF9	VSS_86	B37
AF8	VSS_87	B32
AF7	VSS_88	B31
AF6	VSS_89	N33
AF5	VSS_90	N31
		N27
		N21
		N13
		N10
		N7

LE82BLP-A2/BGA1226

MCHH		
REV:1.3		
N5	VSS_81	B10
M37	VSS_82	VSS_271
M35	VSS_83	VSS_272
M33	VSS_84	VSS_273
M27	VSS_85	VSS_274
M21	VSS_86	VSS_275
M20	VSS_87	VSS_276
M17	VSS_88	VSS_277
M15	VSS_89	VSS_278
M10	VSS_90	VSS_279
M7	VSS_91	VSS_280
M1	VSS_92	VSS_281
L40	VSS_93	VSS_282
L33	VSS_94	VSS_283
L32	VSS_95	VSS_284
L31	VSS_96	VSS_285
L29	VSS_97	VSS_286
L24	VSS_98	VSS_287
L20	VSS_99	VSS_288
L11	VSS_100	VSS_289
L7	VSS_101	VSS_290
L5	VSS_102	VSS_291
L3	VSS_103	VSS_292
K43	VSS_104	VSS_293
K26	VSS_105	VSS_294
K21	VSS_106	VSS_295
K18	VSS_107	VSS_296
K13	VSS_108	VSS_297
K12	VSS_109	VSS_298
K2	VSS_110	VSS_299
J38	VSS_111	VSS_300
J35	VSS_112	VSS_301
J32	VSS_113	VSS_302
J27	VSS_114	VSS_303
J21	VSS_115	VSS_304
J9	VSS_116	VSS_305
J7	VSS_117	VSS_306
J5	VSS_118	VSS_307
H31	VSS_119	VSS_308
H29	VSS_120	VSS_309
H21	VSS_121	VSS_310
H20	VSS_122	VSS_311
H17	VSS_123	VSS_312
H15	VSS_124	VSS_313
H13	VSS_125	VSS_314
G42	VSS_126	VSS_315
G38	VSS_127	VSS_316
G32	VSS_128	VSS_317
G21	VSS_129	VSS_318
G13	VSS_130	VSS_319
G12	VSS_131	VSS_320
G11	VSS_132	VSS_321
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G7	VSS_134	VSS_323
G1	VSS_135	VSS_324
F37	VSS_136	VSS_325
F35	VSS_137	VSS_326
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F3	VSS_142	VSS_331
E43	VSS_143	VSS_332
E32	VSS_144	VSS_333
E24	VSS_145	VSS_334
E21	VSS_146	VSS_335
E11	VSS_147	VSS_336
E9	VSS_148	VSS_337
E3	VSS_149	VSS_338
D40	VSS_150	VSS_339
D31	VSS_151	VSS_340
D21	VSS_152	VSS_341
D17	VSS_153	VSS_342
D15	VSS_154	VSS_343
D3	VSS_155	VSS_344
C26	VSS_156	VSS_345
C11	VSS_157	VSS_346
P43	VSS_158	VSS_347
P30	VSS_159	VSS_348
C6	VSS_160	VSS_349
C5	VSS_161	VSS_350
P18	VSS_162	VSS_351
P17	VSS_163	VSS_352
P2	VSS_164	VSS_353
B37	VSS_165	VSS_354
B32	VSS_166	VSS_355
B31	VSS_167	VSS_356
N33	VSS_168	VSS_357
N31	VSS_169	VSS_358
N27	VSS_170	VSS_359
N21	VSS_171	VSS_360
N13	VSS_172	VSS_361
N10	VSS_173	VSS_362
N7	VSS_174	VSS_363
VSS_179	VSS_175	VSS_364
VSS_180?????	VSS_176	VSS_365

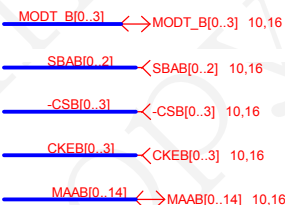
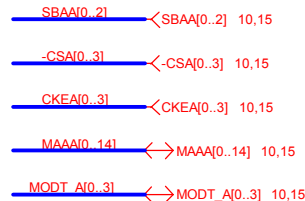
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GMCH-GND		
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GMCH-PWR			
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DDRVTT Decouple



Title **DDRII TERMINATOR**

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Size
Custom

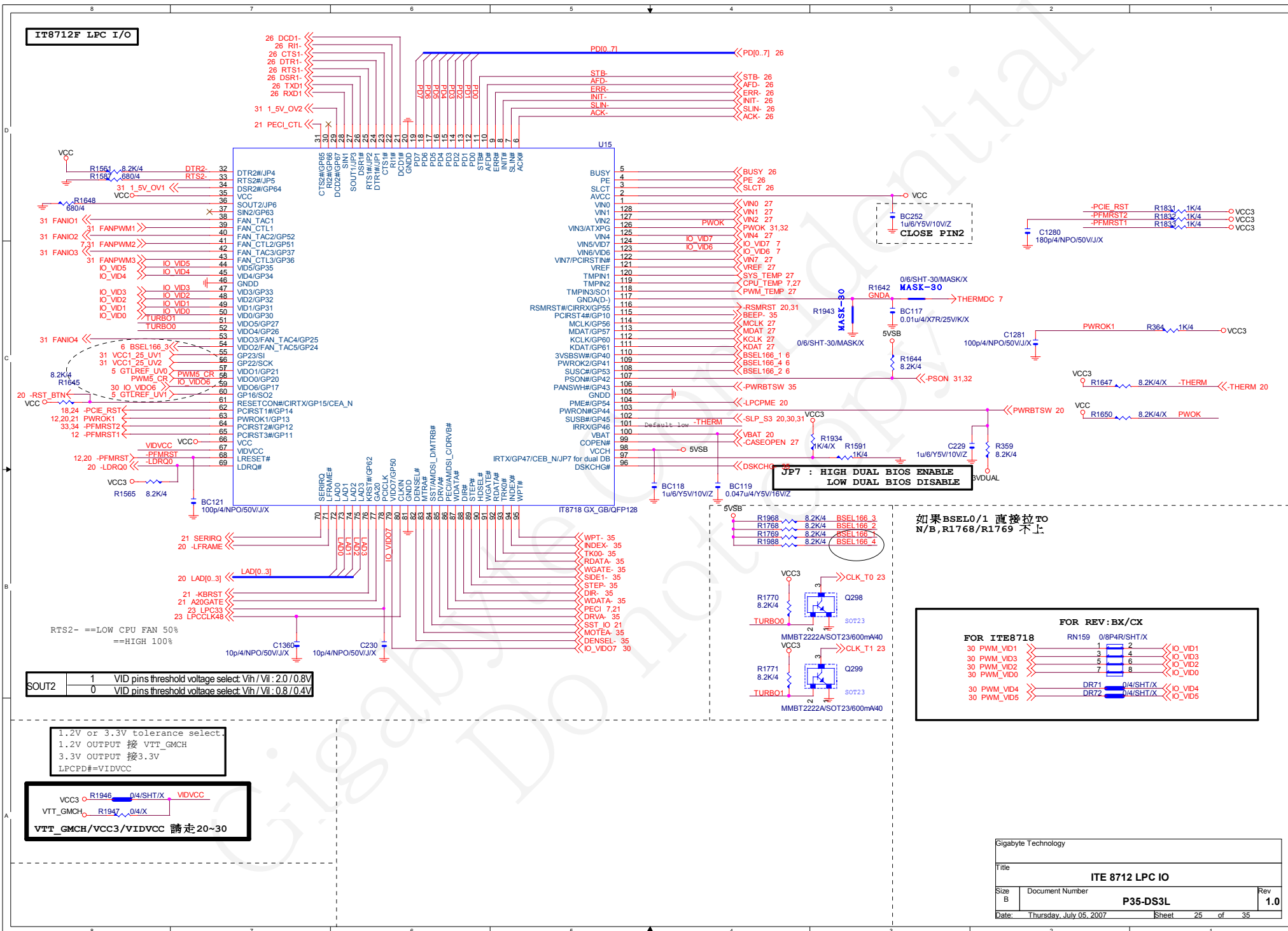
Rev	1.0
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Date: Thursday, July 05, 2007

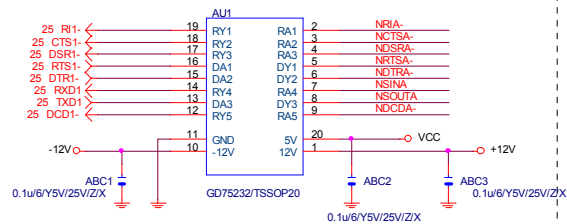
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ITE8712F LPC I/O

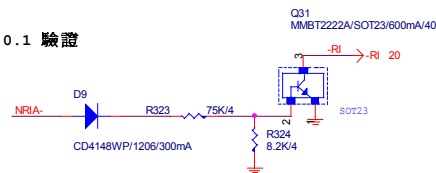


COMA

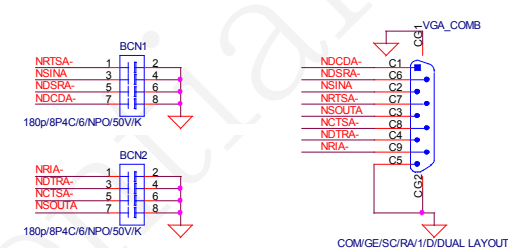


COM RI

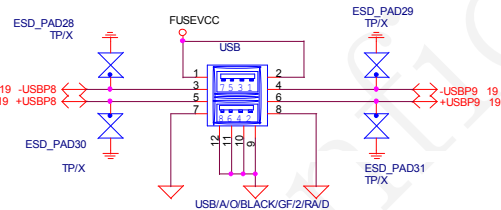
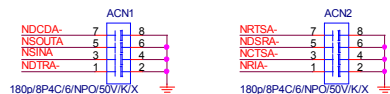
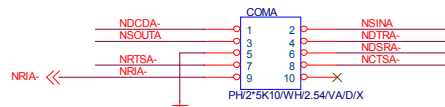
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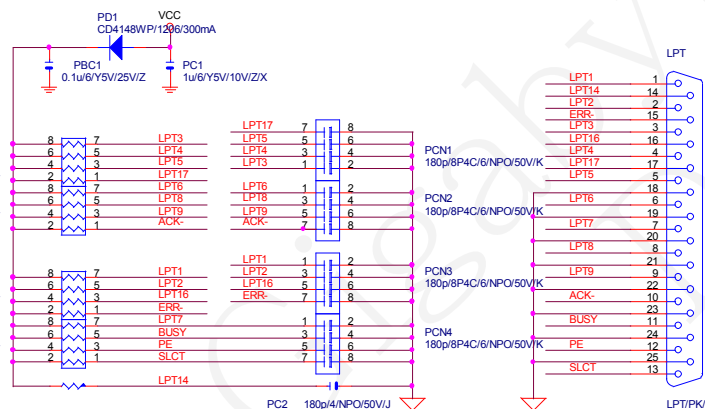
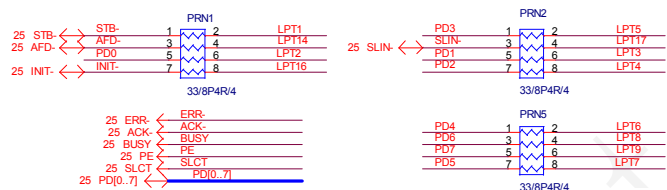
EXTERNAL COMB



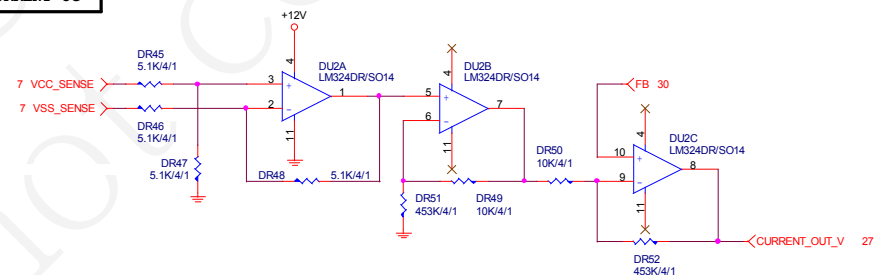
PLACE NEAR VGA_COM CONNECTOR



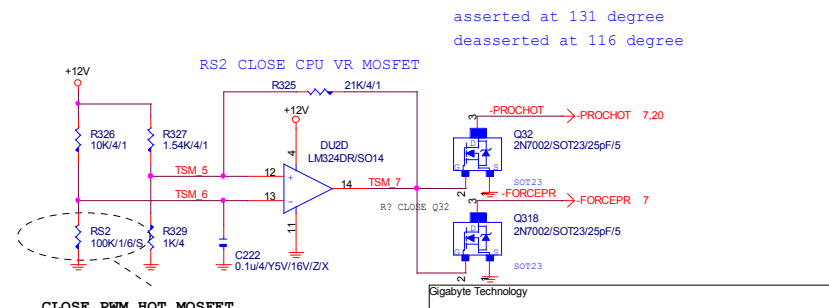
LPT PORT



DYNAMIC CURRENT OC



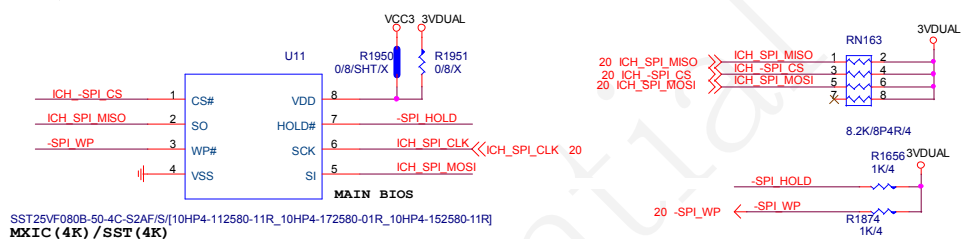
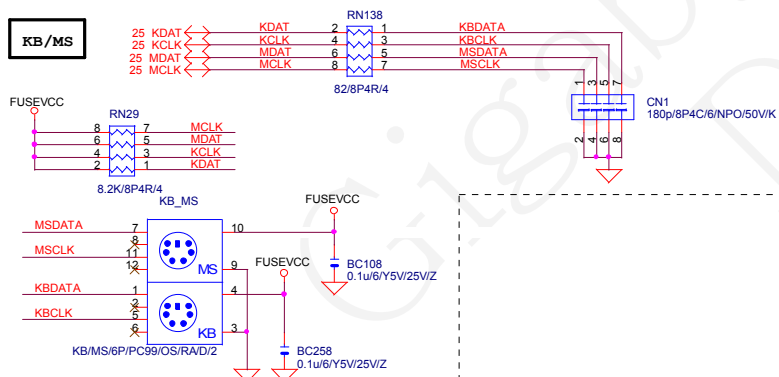
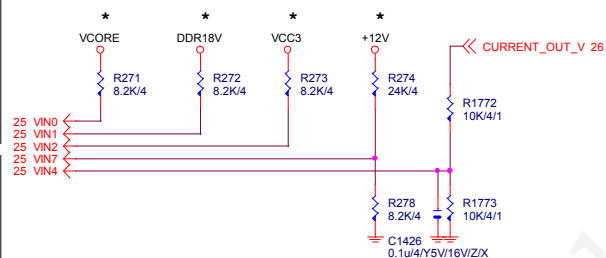
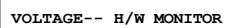
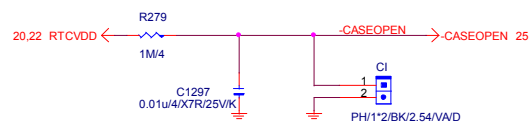
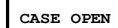
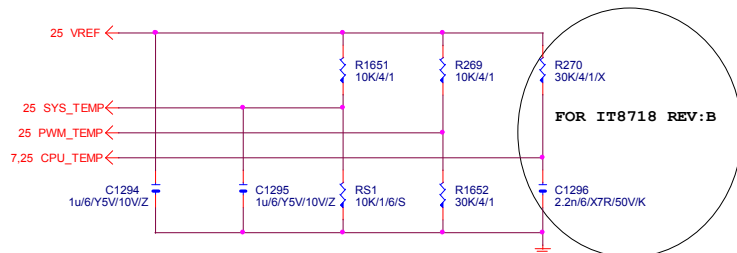
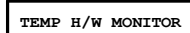
-PROHOT

asserted at 131 degree
deasserted at 116 degree

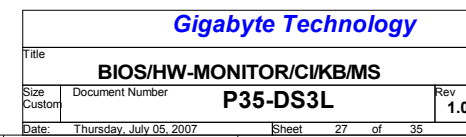
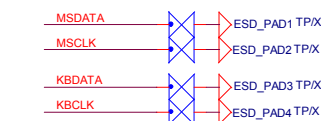
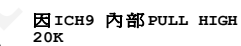
CLOSE PWM HOT MOSFET

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Title		
COM & LPT PORT		
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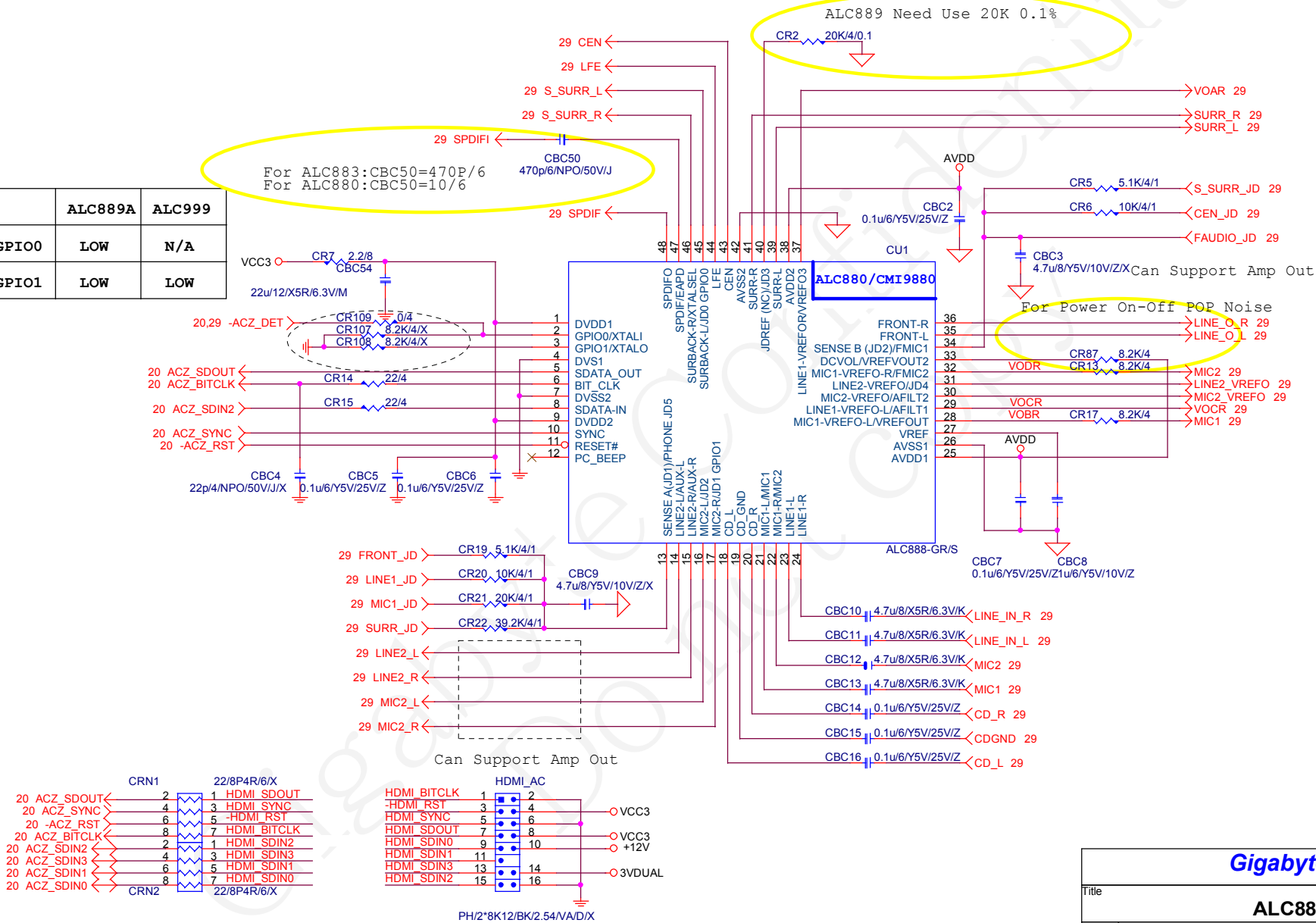


BOOT DEVICE	GNT0	CS1
SPI	0	1
PCI	1	0
FWH	1	1



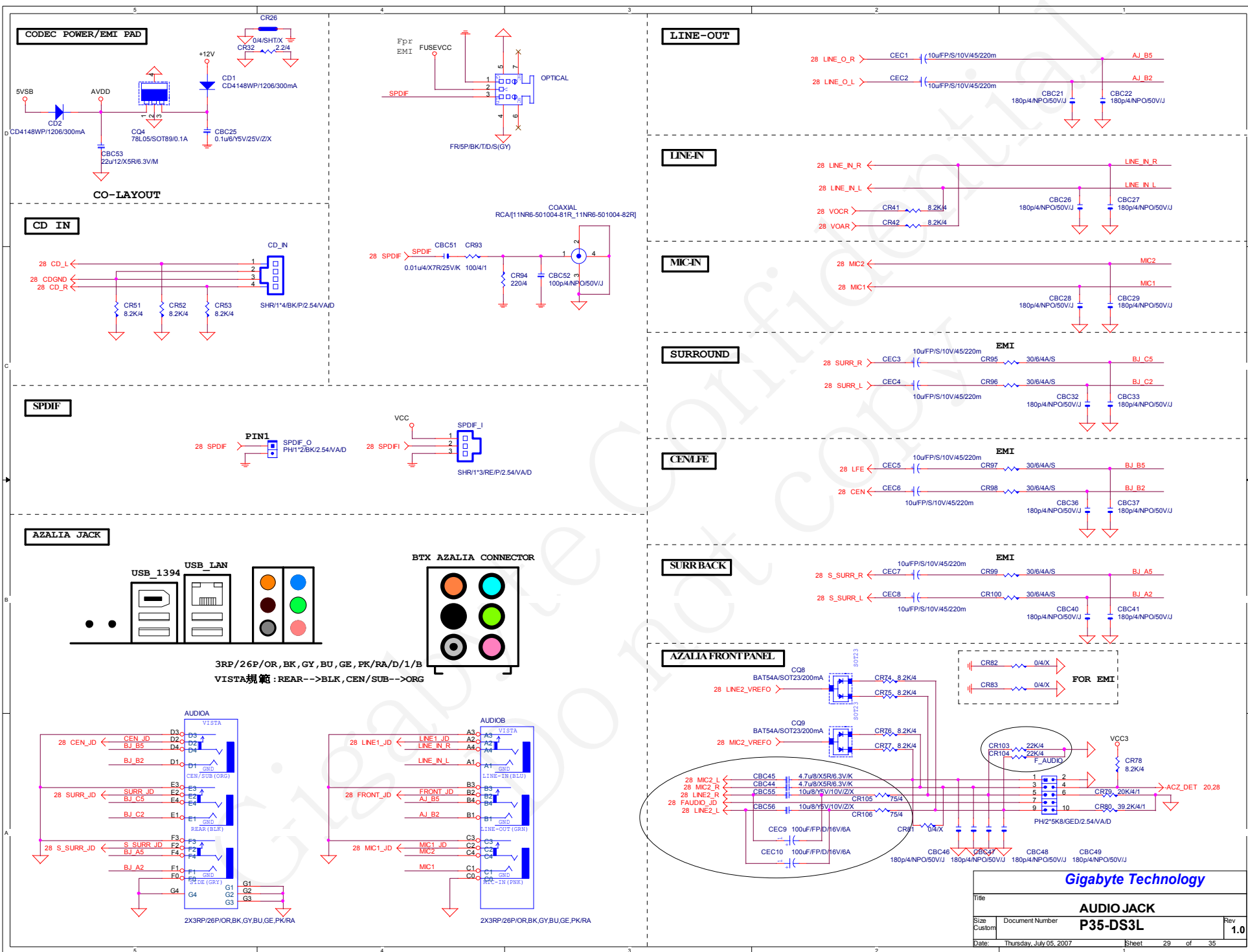
AZALIA CODEC

	ALC889A	ALC999
GPIO0	LOW	N/A
GPIO1	LOW	LOW

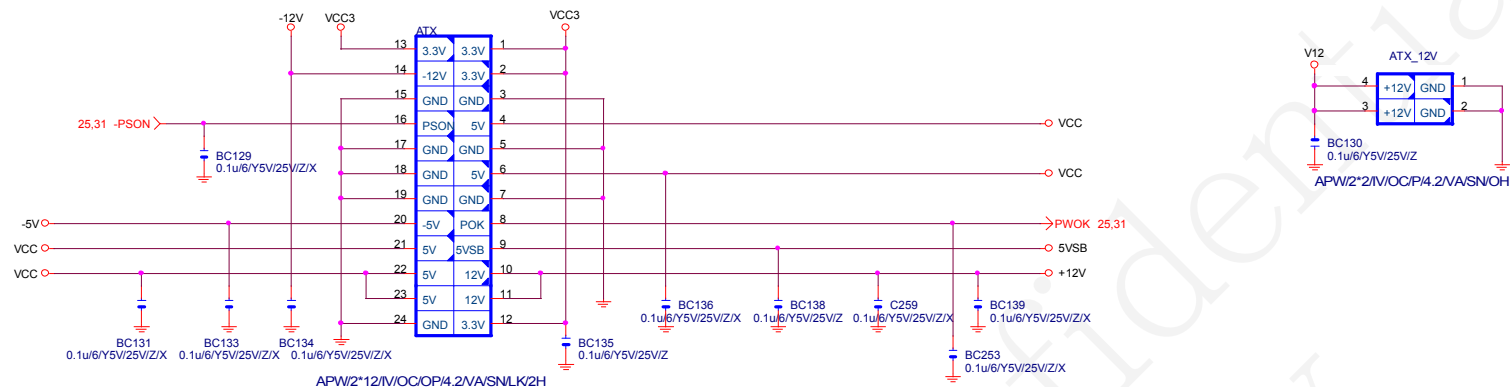


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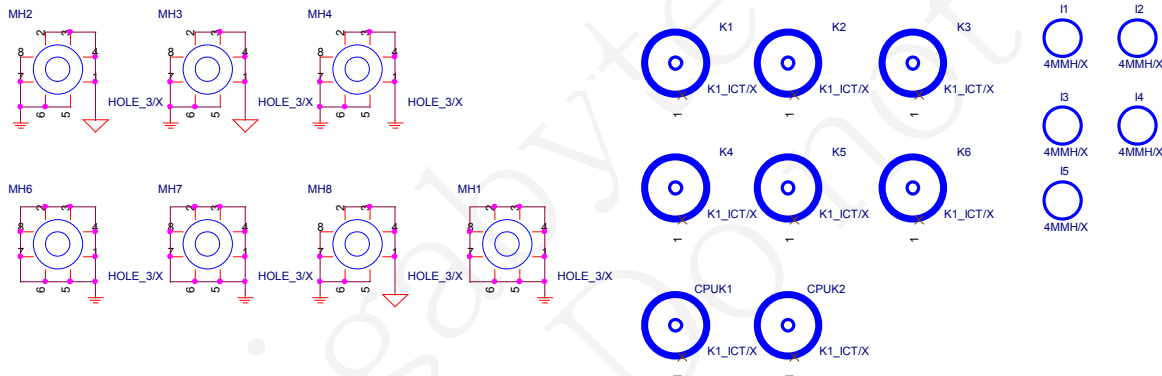
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ALC888			
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ATX POWER CONNECTOR



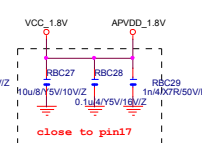
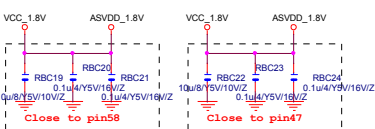
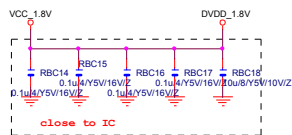
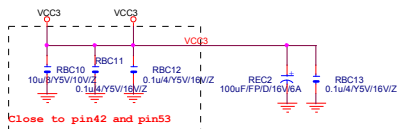
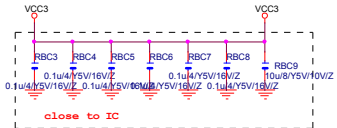
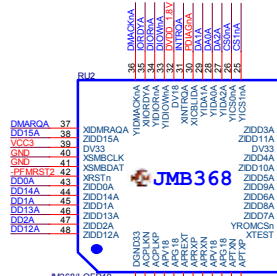
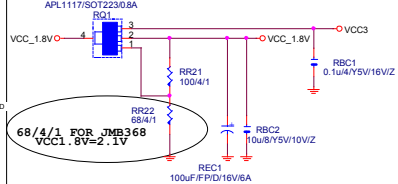
HOLE_3-2--->有鉛



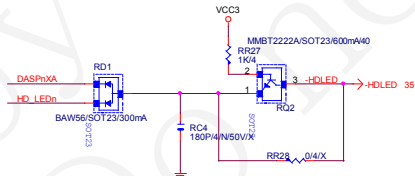
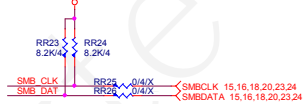
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3.3V to 1.8V Voltage Regulator

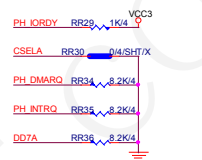
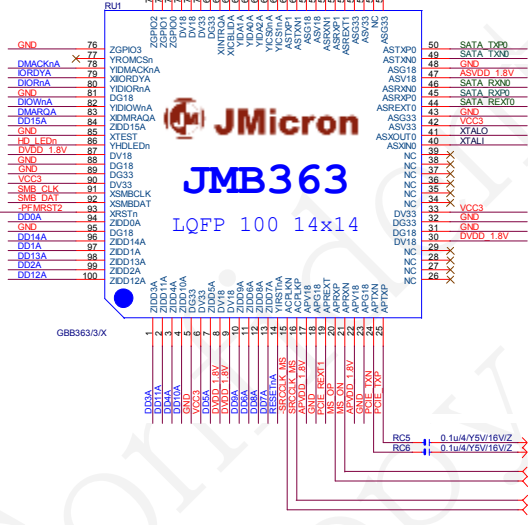


JMB368 REMOVE

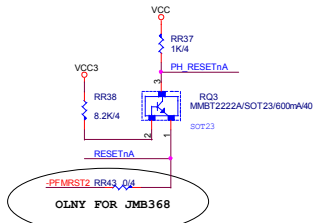
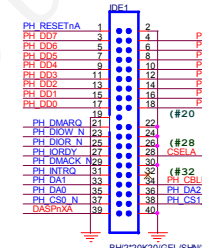


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LQFP 100 14x14



IDE Connector



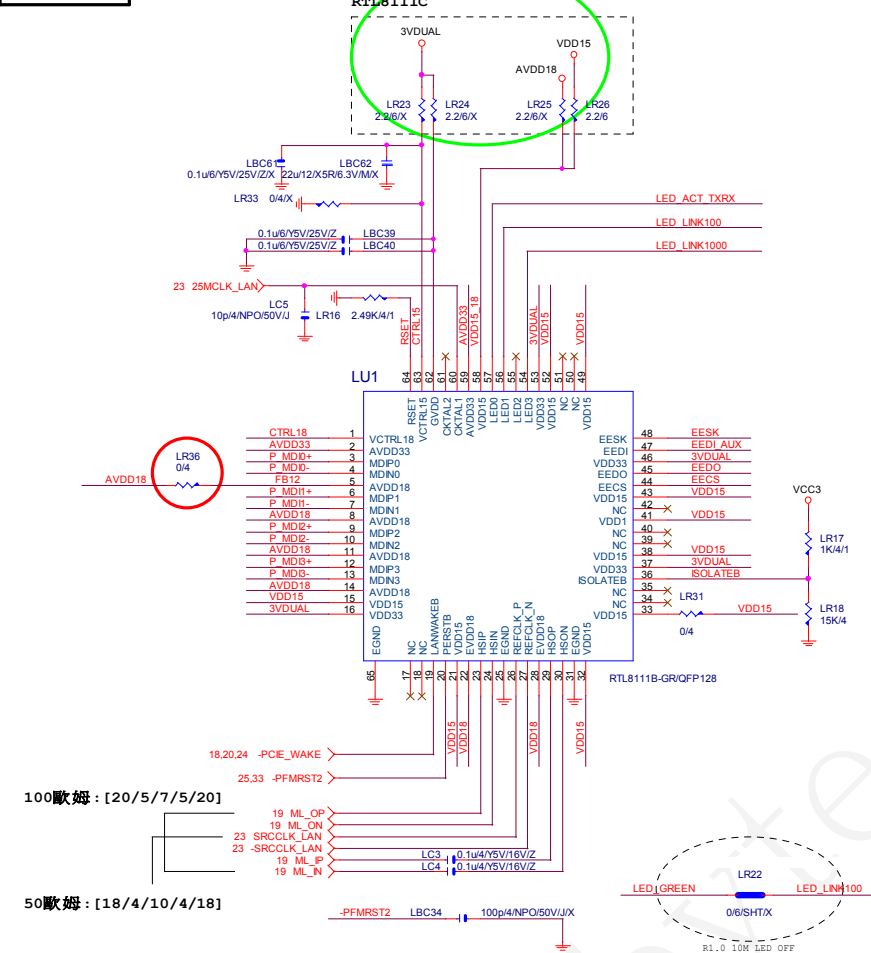
SATA Connector

Impedance=95 +/- 17.5%



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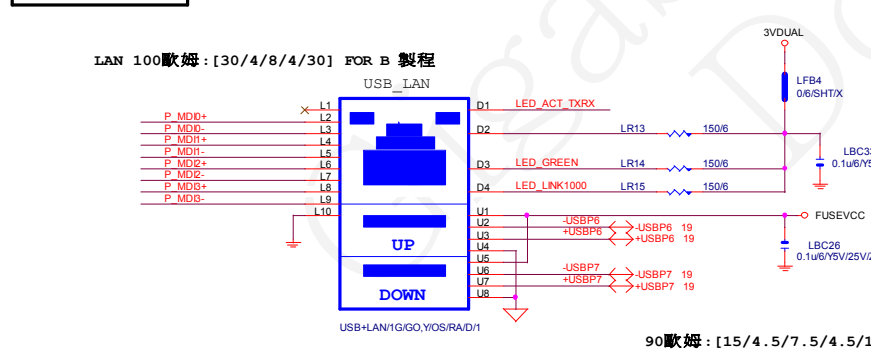
PCIE-1G LAN



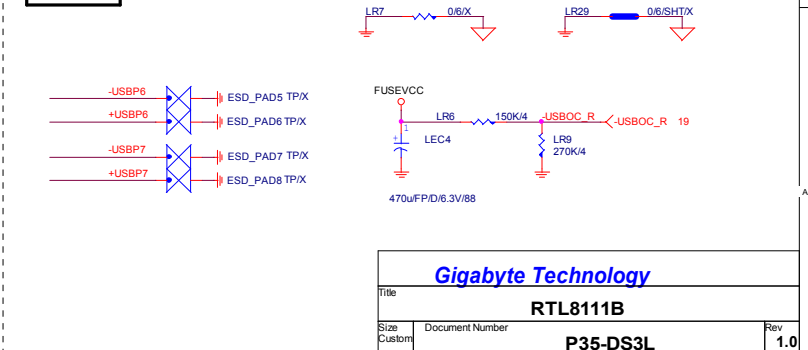
Power domain chart

	RTL8111B / RTL8101E	RTL8111C
AVDD33	3.3V	3.3V
AVDD18	1.8V	1.2V
EVDD18	1.8V	1.2V
DVDD15	1.5V	1.2V

USB_LAN CONNECTOR



USB_LAN



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RTL8111B

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1.0

